

Figure 3

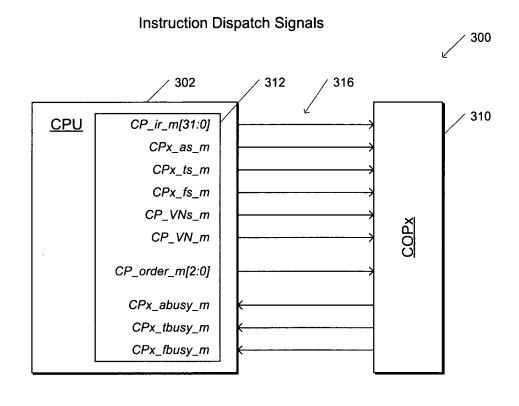
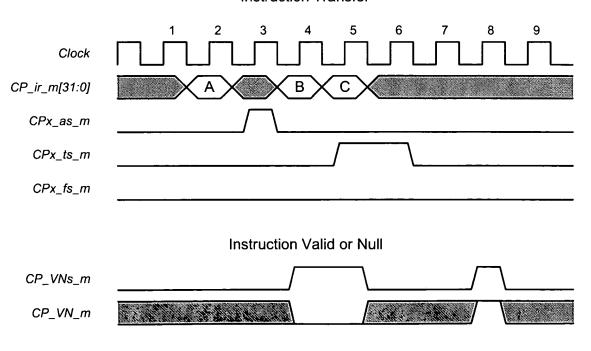


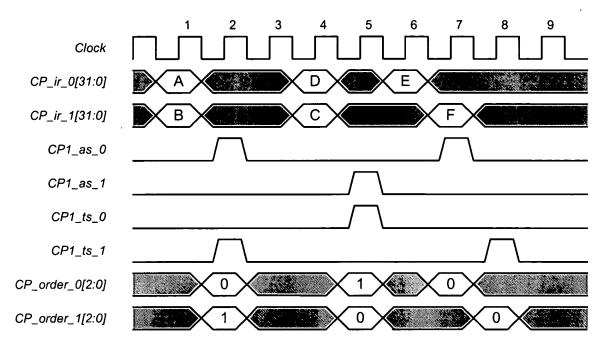
Figure 4

Instruction Transfer

∕. 400









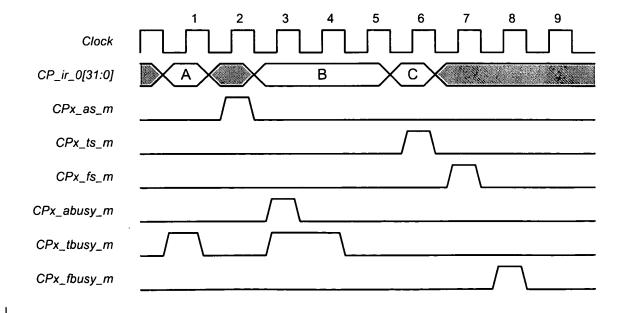
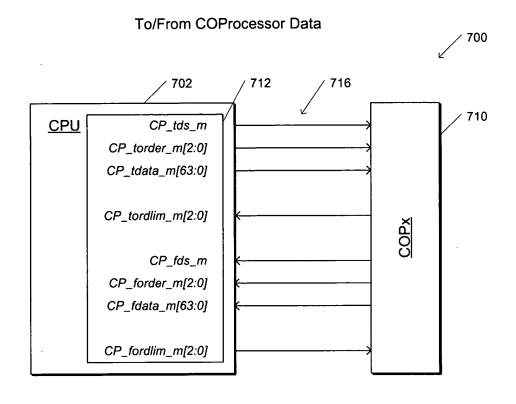


Figure 7



800

Figure 8

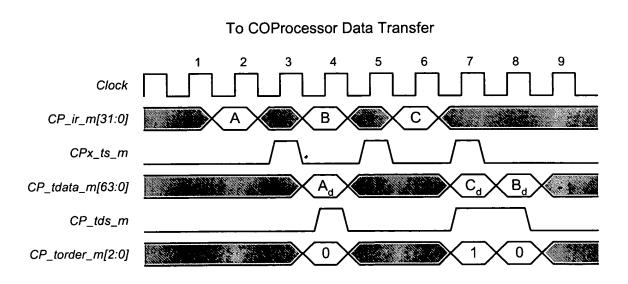


Figure 9



From COProcessor Data Transfer

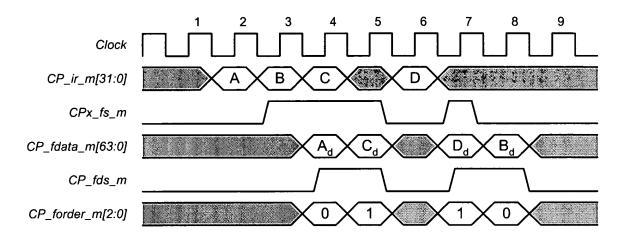


Figure 10

COProcessor Condition Code Check / GPR / exception Found / Instruction Commit

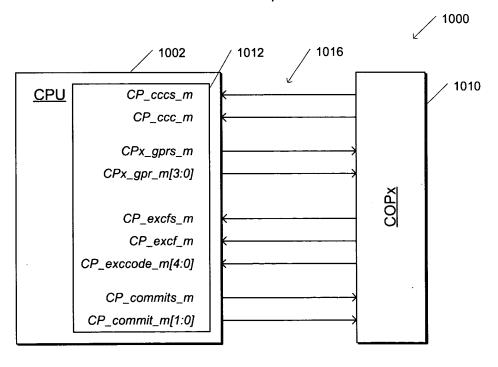


Figure 11

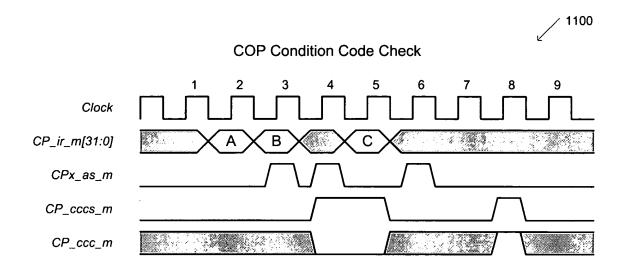


Figure 12

1200

Exception Found

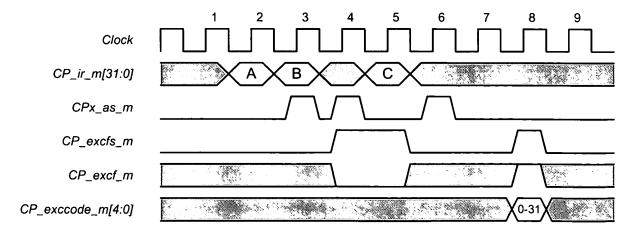


Figure 13

